

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of

Applicant : Karlheinz Freywald
Serial No. : 10/588,415
Filing Date : 07/14/2008
Title : FILLING OF INSULATION TRENCHES USING CMOS-
STANDARD PROCESSES FOR CREATING
DIELECTRICALLY INSULATED AREAS ON A SOI DISK
Docket : LEO 009 PA
Art Unit :
Examiner :

Commissioner for Patents
PO Box 1450
Alexandria VA 22313-1450

Sir:

SECOND SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. §§ 1.56, 1.97, AND 1.98

Supplemental to the Information Disclosure Statements submitted herein on August 4, 2006 and July 14, 2008, Applicant submits herewith patents, publications, and other information of which he is aware, which he believes may be material, as defined in 37 CFR § 1.56(b), to the examination of this application and in respect of which there may be a duty to disclose in accordance with 37 CFR § 1.56(a). While the information referred to in this Second Supplemental Information Disclosure Statement may be material pursuant to 37 CFR § 1.56(b), the filing of this Second Supplemental Information Disclosure Statement is not intended to, pursuant to 37 CFR § 1.97(h), constitute an admission that any patent, publication, or other information referred to is, or is considered to be, material to the patentability of this invention. Further, pursuant to 37 CFR § 1.97(g), the filing of this Second Supplemental Statement should not be construed as a statement that a search has been made or that no other material information exists.

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A listing of all items of information cited is attached on PTO Form PTO/SB/08, substitute form for 1449 /PTO.

This Supplemental Information Disclosure Statement is being filed within the period set forth in 37 CFR § 1.97(b) because it is believed to be filed before the mailing of a first Office action on the merits.

Respectfully submitted,

STEVENS & SHOWALTER, L.L.P.

By: /Richard C. Stevens/
Richard C. Stevens
Registration No. 28,046

7019 Corporate Way
Dayton OH 45459-4238
Telephone: (937) 438-6848
Facsimile: (937) 438-2124
E-mail: rstevens@sspatlaw.com

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